

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,894,319 B2
DATED : May 17, 2005
INVENTOR(S) : Takashi Kobayashi et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

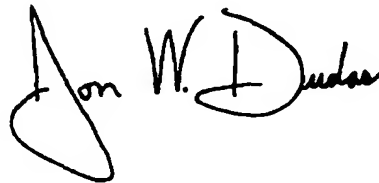
Title page,

Item [54], Title, should read -- **VERTICAL INSULATED GATE
SEMICONDUCTOR DEVICE** --

Item [30], **Foreign Application Priority Data**, should read
-- October 31, 2000 JP 2000-331840
November 28, 2000 JP 2000-361106 --

Signed and Sealed this

Nineteenth Day of July, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office